

SEMIPACK® 3

Thyristor Modules

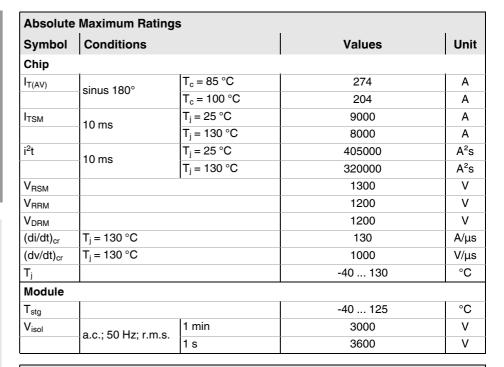
SKKT 273/12 E

Features*

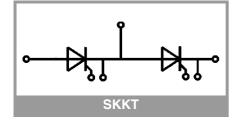
- · Industrial standard package
- · Electrically insulated base plate
- Heat transfer through aluminum oxide ceramic insulated metal base plate
- Chip soldered on direct copper bonded Al₂O₃ ceramic
- UL recognition, file no. E63532

Typical Applications

- DC motor control (e. g. for machine tools)
- Temperature control (e. g. for ovens, chemical processes)
- Professional light dimming (studios, theaters)



Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Chip	•					•
V _T	T _j = 25 °C, I _T = 750 A				1.6	V
$V_{T(TO)}$	T _j = 130 °C				0.90	V
r _T	T _j = 130 °C				0.92	mΩ
I _{DD} ;I _{RD}	$T_j = 130$ °C, $V_{DD} = V_{DRM}$; $V_{RD} = V_{RRM}$				100	mA
t _{gd}	$T_j = 25 ^{\circ}\text{C}, I_G = 1 \text{A}, di_G/dt = 1 \text{A}/\mu\text{s}$			1		μs
t _{gr}	$V_{D} = 0.67 * V_{DRM}$			2		μs
tq	T _j = 130 °C			150		μs
I _H	T _j = 25 °C			150	500	mA
IL	$T_j = 25 ^{\circ}\text{C}, R_G = 33 \Omega$			300	2000	mA
V_{GT}	$T_j = 25$ °C, d.c.		2			V
I _{GT}	$T_j = 25$ °C, d.c.		150			mA
V_{GD}	$T_j = 130 ^{\circ}\text{C}, \text{d.c.}$				0.25	V
I _{GD}	T _j = 130 °C, d.c.				10	mA
R _{th(j-c)}	cont.	per chip			0.104	K/W
		per module			0.052	K/W
$R_{th(j-c)}$	sin. 180°	per chip			0.108	K/W
		per module			0.054	K/W
R _{th(j-c)}	rec. 120°	per chip			0.122	K/W
		per module			0.061	K/W
Module						
R _{th(c-s)}	chip module			0.08		K/W
				0.04		K/W
Ms	to heatsink M5		4.25		5.75	Nm
M _t	to terminals M8		7.65		10.35	Nm
а					5 * 9.81	m/s²
w				410		g



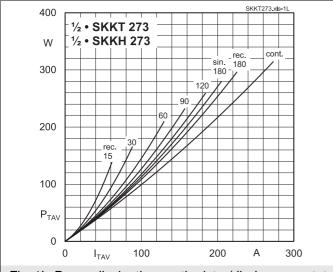


Fig. 1L: Power dissipation per thyristor/diode vs. on-state current

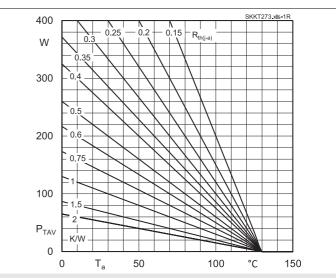


Fig. 1R: Power dissipation per thyristor/diode vs. ambient temperature

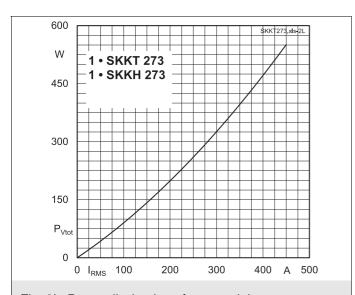


Fig. 2L: Power dissipation of one module vs. rms current

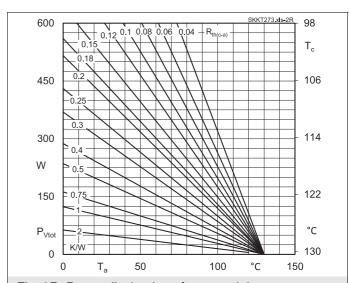
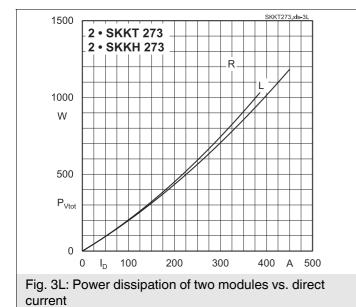
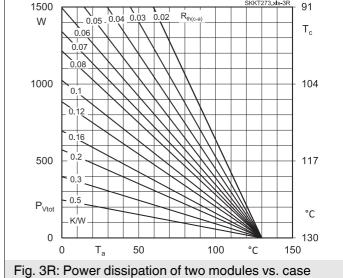


Fig. 2R: Power dissipation of one module vs. case temperature





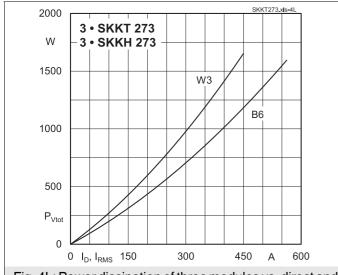


Fig. 4L: Power dissipation of three modules vs. direct and rms current

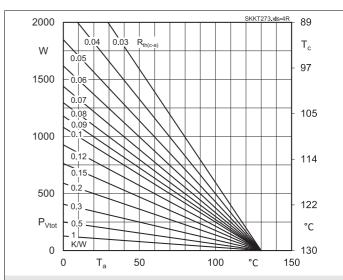


Fig. 4R: Power dissipation of three modules vs. case temperature

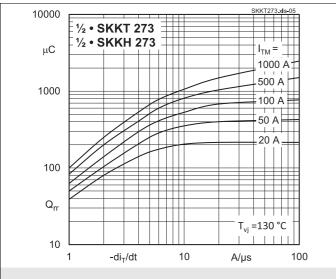


Fig. 5: Recovered charge vs. current decrease

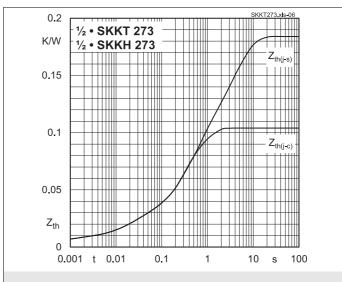


Fig. 6: Transient thermal impedance vs. time

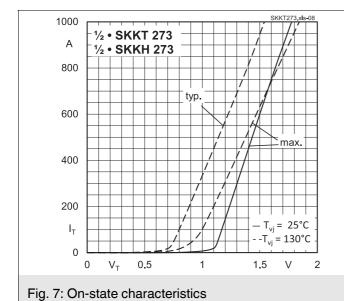
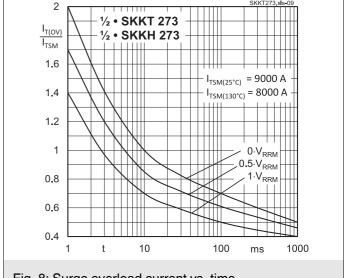
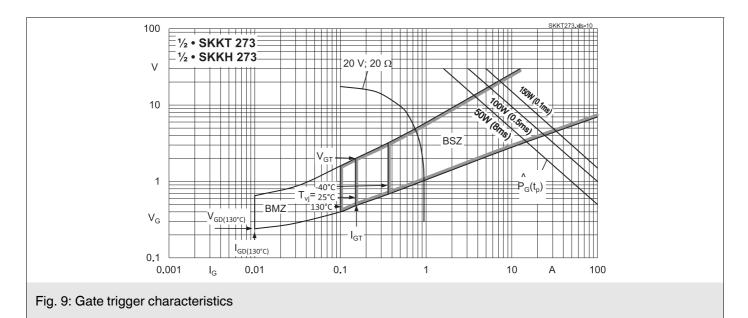
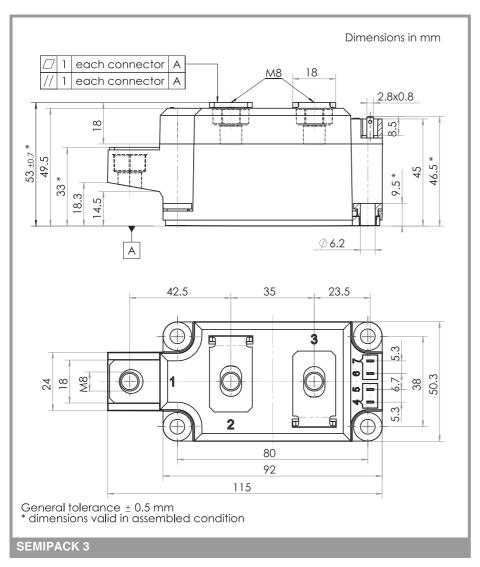
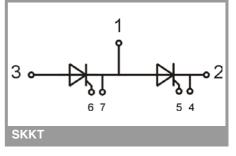


Fig. 8: Surge overload current vs. time









This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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